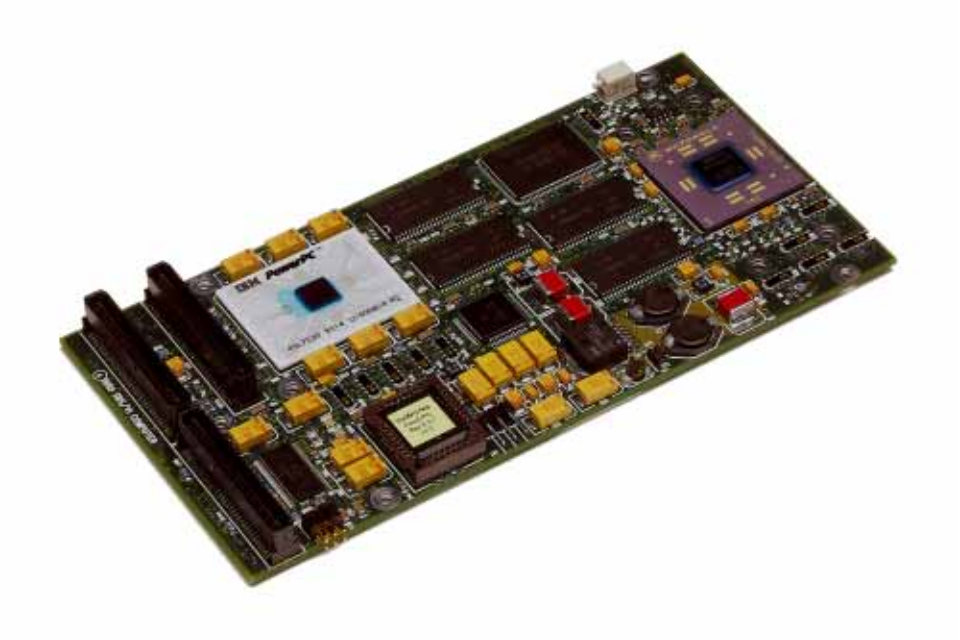
Palomar II
PowerPC™ 750 in a PMC Module Package

Features:

- PPMC Compatible
- PowerPC 750 @ 366MHz
- 64MB SDRAM with ECC
- 1MB L2 Cache
- 4.5MB FLASH
- Two Independent I²C Serial Bus Lines
- Two RS-232C Serial Ports
- JTAG Debug Support
- Watchdog Timer
- PMC Compliant form
- Built-In Test (BITe)
- VxWorks support
- Up to 4 Palomar II's may be hosted on a carrier card



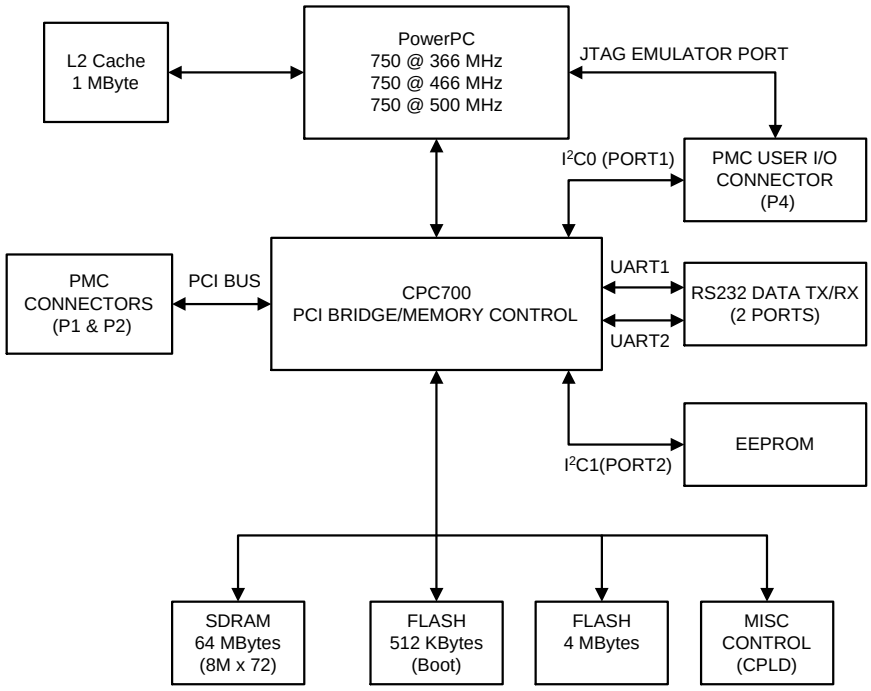
Palomar II ™ packs the performance of the PowerPC 750 processor and 64MB of SDRAM into a PMC compliant form-factor (149mm x 74mm). The Palomar II design includes 1MB L2 Cache to boost application performance.

Many telecom/datacom customers today are designing carrier cards that employ the PMC mezzanine standard for processor and I/O expansion. Palomar II is an ideal processor engine for these specialized applications requiring high performance processors supporting the host card. Processing requirements can be off-loaded from the host card to one to four Palomar II's to increase the total CPU performance per customer system slot.

Two RS-232C ports, two I²C serial bus lines, watchdog timer, and JTAG connection are provided for development and application debugging purposes on each Palomar II module.

Palomar II is currently supported with VxWorks, but other real time kernels could be ported, based on your system requirements.

Palomar II Block Diagram



Palomar II Specifications

General	Model	Palomar II
	Form Factor	PMC, IEEE 1386
Processor	Clock Rate	366MHz PowerPC 750
	L1 Cache	32KB DATA and 32KB INSTRUCTION
PCIbus	Controller	CPC700, 32 Bit, 33 MHz Synchronous or 66 MHz Asynchronous
Memory	SDRAM	64MB Synchronous DRAM with ECC on PMC Base Memory Timing 2-1-1-1, Memory Bus Speed is 66.6 MHz
	L2 Cache	1MB Pipelined Burst SRAM Dedicated L2 bus for very high performance
	FLASH	512KB Boot PROM - 32 pin PLCC socketed device
		4MB soldered on FLASH, 32 128KB sectors, May be Configured as BootROM
Serial I/O	Controller	2 UARTs, NS16550 Compatible
	Type	2 RS-232C, (1) on card, (1) through PMC User I/O connector (P4)
JTAG		JTAG Emulator/Debug (P4)
Software	O/S Support	VxWorks available today, LynxOS, pSOS+, and Open BSD/RT planned
Electrical	Power	+5VDC - 7.5 Watts
Environmental	Temperature	0 to +55C Inlet Air Operating, -40 to +85C Non-Operating
	Cooling	Forced Air 100 LFM Minimum Required
	Humidity	10 to 95% Relative Humidity, Non-condensing
Regulatory		UL1950, NEBS - Zone 4;FCC, Part 15, Class B; EN55022, Class B; Emissions EN50082-1 Immunity

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